

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Atty. Docket

JEROEN LEIJTEN ET AL

NL 000576

Serial No.

Filed: CONCURRENTLY

DIGITAL SIGNAL PROCESSING APPARATUS

Commissioner for Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

Prior to calculation of the filing fee and examination,
please amend the above-identified application as follows:

IN THE CLAIMS

Please amend the claims as follows:

3. (Amended) Apparatus according to claim 1, comprising first state buffer means for storing the current state of hardware resource means in case of an interrupt, characterized in that in case of an interrupt said first state buffer means stores the current state of at least a part of such hardware resource means which are not included in said predetermined limited subset of said hardware resource means.

6. (Amended) Apparatus according to claim 4, characterized in that at least a part of such hardware resource means which are not directly accessible and not included in said predetermined limited subset of said hardware resource means are chained together in a first scan chain means and at least a part of such hardware resources which are not directly

accessible and included in said predetermined limited subset of said hardware resource means are chained together in a second scan chain means.

7. (Amended) Apparatus according to claim 1, characterized in that said second instruction set means does not allow operations in parallel.

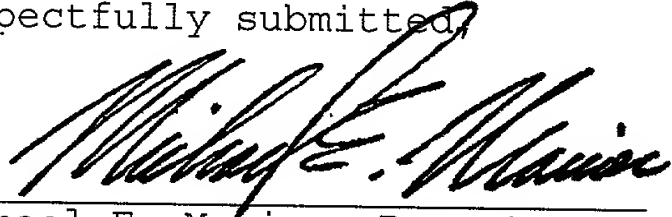
12. (Amended) Method according to claim 8, characterized in that said second instruction set does not allow operations in parallel.

REMARKS

The foregoing amendments to claims 3, 6-7 and 12, were made solely to avoid filing the claims in the multiple dependent form so as to avoid the additional filing fee.

The claims were not amended in order to address issues of patentability and Applicant respectfully reserves all rights he may have under the Doctrine of Equivalents. Applicant furthermore reserves his right to reintroduce subject matter deleted herein at a later time during the prosecution of this application or continuing applications.

Respectfully submitted,

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APPENDIX

3. (Amended) Apparatus according to claim 1 ~~or 2~~, comprising first state buffer means for storing the current state of hardware resource means in case of an interrupt, characterized in that in case of an interrupt said first state buffer means stores the current state of at least a part of such hardware resource means which are not included in said predetermined limited subset of said hardware resource means.

6. (Amended) Apparatus according to claim 4 ~~or 5~~, characterized in that at least a part of such hardware resource means which are not directly accessible and not included in said predetermined limited subset of said hardware resource means are chained together in a first scan chain means and at least a part of such hardware resources which are not directly accessible and included in said predetermined limited subset of said hardware resource means are chained together in a second scan chain means.

7. (Amended) Apparatus according to claim 1 ~~at least any one of the preceding claims~~, characterized in that said second instruction set means does not allow operations in parallel.

12. (Amended) Method according to claim 8 ~~at least any one of claims 8 to 11~~, characterized in that said second instruction set does not allow operations in parallel.